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Ahmad Pourjabbar Kari

Education:

- Ph.D., Computer Engineering-Computer Architecture, Islamic Azad University-Tehran North branch, Tehran, Iran, 2014 -2019.
- Area of Specialization: Artificial Intelligence, Parallel Processing, Advanced Microprocessors Designing.
- Comprehensive Exam GPA: 19.38 of 20 (A).
- Graduate courses: Parallel Programming (A), Advanced Operating Systems (A), Advanced Computer Networks (A), Real Time Systems (A), Advanced Microprocessors Designing (A), Decision Support Systems (A).
- M.Sc., Computer Engineering-Computer Systems Architecture, Computer Engineering Department, East Azarbaijan University of Science and Research, Tabriz, Iran, 2011-2013.
- Area of Specialization: Artificial Intelligence, Nonlinear Control Systems, Neuro-Fuzzy Controllers, Integrated Circuits, Modeling and Design Methods, VLSI designing.
- **GPA:** 19.26 of 20 (A).
- **Thesis:** Presenting a New Neuro-Fuzzy Controller Architecture and Hardware Implementation for Anti-Lock Braking System .
- **Supervisors:** Prof. A. Habibizad Navin (University of Science and Research), and Prof. M. K. Mirnia (University of Tabriz).
- Graduate courses: Advanced VLSI Design (A), Hardware Modeling and Design Methods (A), Advanced Computer Architecture (A), Test and Research Design (A), Advanced Computer Networks(A), Arithmetic Processors (A), Advanced Mathematics in Computer Engineering (A), Artificial Intelligence (A).
- B.Sc., Electronics, Electrical Engineering Department, Islamic Azad University of Tabriz, Tabriz, Iran, 1997-2001.
- Area of Specialization: Electronics, Developing Micro-Processor/Controller based systems for various application.
- **GPA:** 16.84 of 20 (A).
- Thesis: Design and implementation of a Digital PID Controller for DC Motor.
- Supervisor: Prof. Ghasem Alizadeh (University of Tabriz)
- Undergraduate Courses: I have passed 144 credits, some of them are as follows: Electrical circuits I (B), Electrical Circuits II (A), Power Electronics (A), Power System Analysis I (A), Electronics I(A), Electronics I Lab. (A), Electronics II (A), Electronics II Lab. (A), Electronics II (B), Electronics II Lab. (A), Electric Machines I (B), Communication Machines I Lab. (A), Electric Machines II (A), Communication I (B), Communication Circuits Lab. (A), Pulse Technique (A), Pulse Technique Lab. (A), Digital Circuits Design (B), Digital Circuits Design Lab. (A), Microprocessor I (B), Microprocessor I Lab. (A), Physics of Electronics (C), Electronic systems Design (B), Electric Workshop (A), Digital Control Systems (A), Linear Control Systems (B), Linear Control Systems Lab. (A), Signals and Systems (A), Electromagnetics (A), Numerical Computation (A).

B.Sc., Applied Mathematics, Mathematics Department, University of Tabriz, Tabriz, Iran, 1998-2003.

- Area of Specialization: Numerical Analysis, Differential Equation.
- GPA: 16.01 of 20 (A).
- Thesis: Graph Application on Electronic Circuits Analysis.
- Supervisor: Prof. Ayaz Eissazadeh (University of Tabriz)
- Undergraduate Courses: I have passed 136 credits, some of them are as follows: Mathematical Analysis I (A), Mathematical Analysis II (A), Differential Equations (B), Linear Algebra I (A), Advanced Programming (A), Algebra I (B), Graph Theory (A), Numerical Analysis I (B), Numerical Analysis II (A), Operational Research (A), Probability Theory I (A), Probability Theory II (A), Complex Functions (A), Fuzzy Mathematics (A).

Publications:

- A. Pourjabbar Kari, A. Habibizad Navin, M. K. Mirnia "Presenting a new Controller For Anti-lock Braking System", 10 th National Conference on Computer and Intelligent Systems, Amir Kabir University of Technology, Issue 32, Vol. 6, Summer 2013, pp.612-619.
- A. Pourjabbar Kari, A. Habibizad Navin, M. K. Mirnia "Review on Intelligent Braking Systems", 10 th National Conference on Computer and Intelligent Systems, Amir Kabir University of Technology, Issue 32, Vol. 6, Summer 2013, pp.536-542.

Research Interests:

- Designing and Implementation of FPGA, CPLD, AVR and ARM Microcontrollers based Systems, VLSI designing, Robotics, Industrial Automation, Artificial Intelligence, Neuro-Fuzzy Control Systems.
- Nonlinear Optics, Electro-optics, Optoelectronics.
- Next Generation Networks (NGN), Advanced Computer Networks, Communication Systems.

Honors and Awards:

- B.Sc., Tuition fee award, University of Tabriz, Ministry of Science, 1998 2003.
- Ranked 1, the 2013 graduated M.SC. Computer Eng. students at East Azarbaijan University of Science and Research.
- Ranked 3, the 2001 graduated B.SC. Electronics Eng. students at Islamic Azad University of Tabriz.
- Ranked 7, the 2004 graduated B.SC. Applied Mathematics students at University of Tabriz.
- Ranked 79, the 2011 nationwide admission exam (Graduate Study), in Computer Engineering, IRAN.

Work Experience:

Electrical and Communication Eng., ITMC (Iranian Telecommunication Manufacturing Company), Shiraz, Iran, 02/07- up to now.

- Engineering and commissioning of Siemens and ZTE Communication systems, Next Generation Networks, Asterisk Soft Switch and VOIP networks.
- Instructor, Tabriz Universities, Tabriz, Iran, 09/14 up to now.
- Teaching Courses: Computer Architecture, C++ Programming language, Logical Circuits, Microprocessor Lab., Computer Architecture Lab, Digital Systems Lab., Algorithm Design, Differential Equations, Engineering Mathematics(Technomath), Discrete Mathematics, System Programming.

Electrical Eng., Novin Control Co., Tabriz, Iran, 12/05-01/07

 Designing, engineering, implementation and commissioning of industrial automation systems for automotive industries such as at EURO (Esfahan Urban Railway Organization) and Implementation of control systems by FPGA, CPLD, AVR and ARM Microcontrollers.

Electrical Eng., Pars Electro Co., Tabriz, Iran, 3/02-11/05

 Basic designing of power supply systems. Responsible for designing, engineering, implementation and commissioning of control systems.

Certifications and courses:

- **NGN:** Level A of Next Generation Networks (NGN) in ZTE University (Shenzhen, China).
- HDL Programming Courses: Verilog and VHDL programming course and hardware implementation by FPGA's.
- Advanced Siemens EWSD Communication Systems Courses: EWSD Local and Transit communication switches, MSC (Mobile Switching Center), HLR (Home Location Register), Siemens transmission systems.
- ZTE Communication Switches Courses: ZXJ10 Local and Transit communication switches.
- Microsoft EXCEL

Languages:

- Farsi
- English
- Turkish

Soft wares:

- Verilog
- VHDL
- Cadence
- Xilinx ISE Design Suit
- ORCAD
- H Spice
- MATLAB/Simulink
- LabVIEW
- Altium Designer
- C++
- Python
- Intel Microprocessor Programming
- Atmel Microcontroller Programming
- Microsoft Excel, Word, Outlook and Power point
- PLC: Fatek and LG
- ARM Microcontroller Programming
- Asterisk and Elastix Programming
- Packet Tracer
- Code Vision
- Proteus